



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Docket No.: NVID-P000406 I hereby certify that this transmittal of the below described document is being deposited with the United States Postal Service in an envelope bearing First Class Postage and addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the below date of deposit

Signature of the Person 06/28/05 Name of Person Date of Mina Oliveri Making the Deposit: Making the Deposit: Deposit:

Inventor(s):

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Serial No.:

10/078,292

Group Art Unit:

2863

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Examiner:

Sun, Xiuqin

Confirmation No:

3407

Title:

A METHOD AND SYSTEM FOR DYNAMIC POWER SUPPLY VOLTAGE ADJUSTMENT

FOR A SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

TRANSMITTAL OF FORMAL DRAWINGS

In response to Drawing Informalities

attached please find:

the formal drawings for this application

Number of

6/28/2005

9

Sheets

Each sheet of drawing indicates the identifying indicia suggested in § 1.84(c) on the

reverse side of the drawing

a copy of the NOTICE OF INFORMAL DRAWINGS

Please direct all correspondence concerning the above-identified application to the following address:

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Respectfully submitted,

Anthony C. Murabito Reg. No. 35,295